

Figure 1

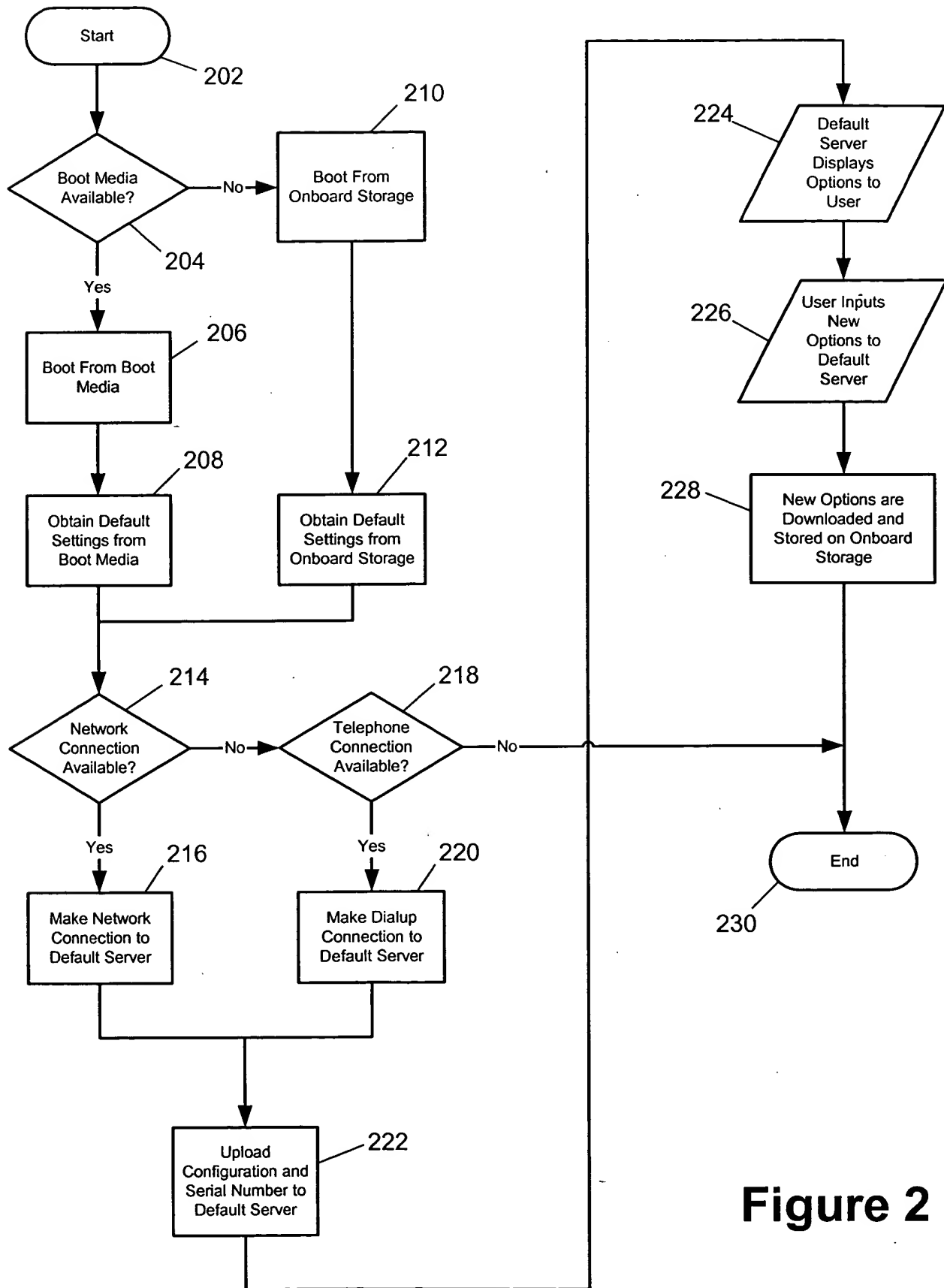


Figure 2

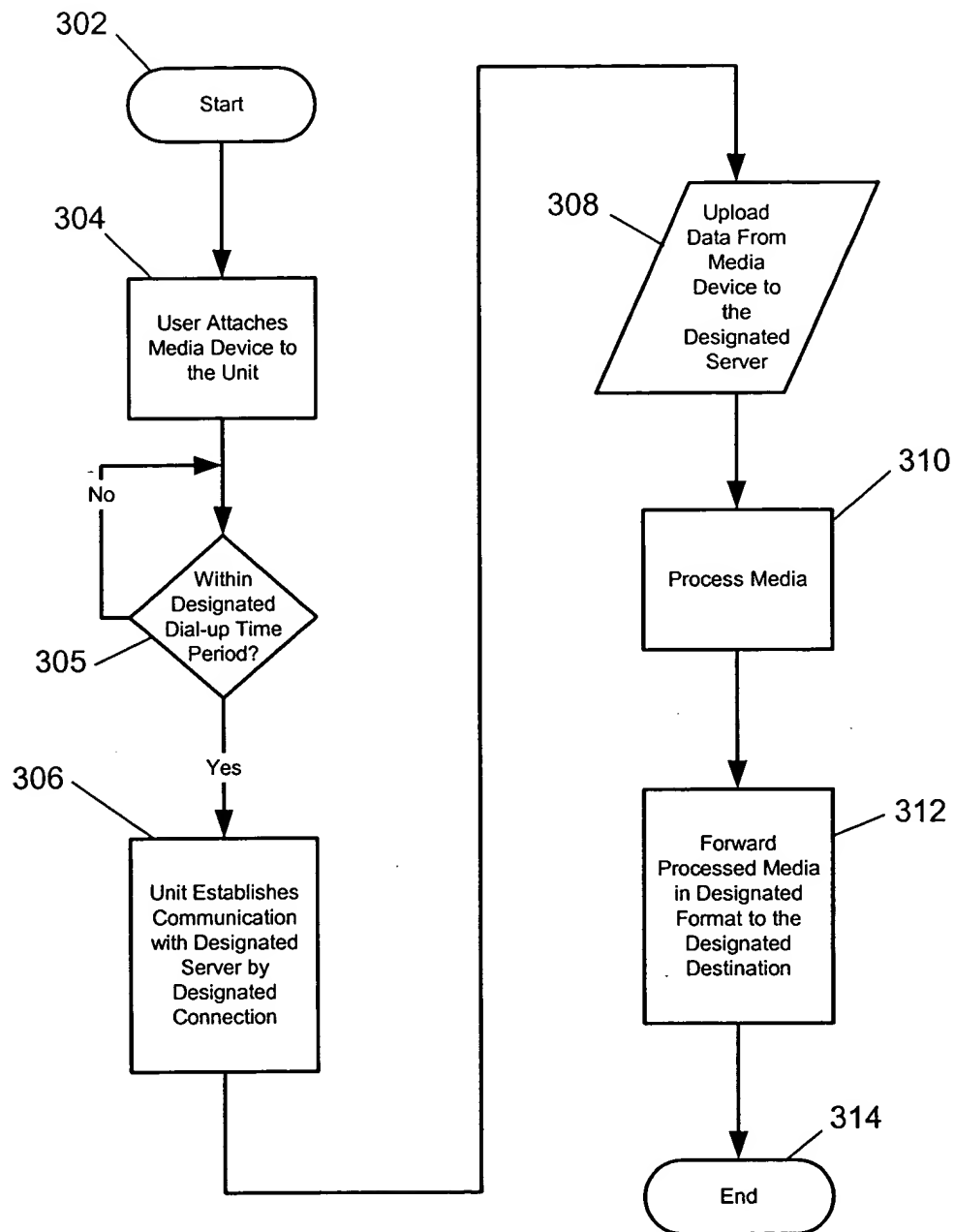
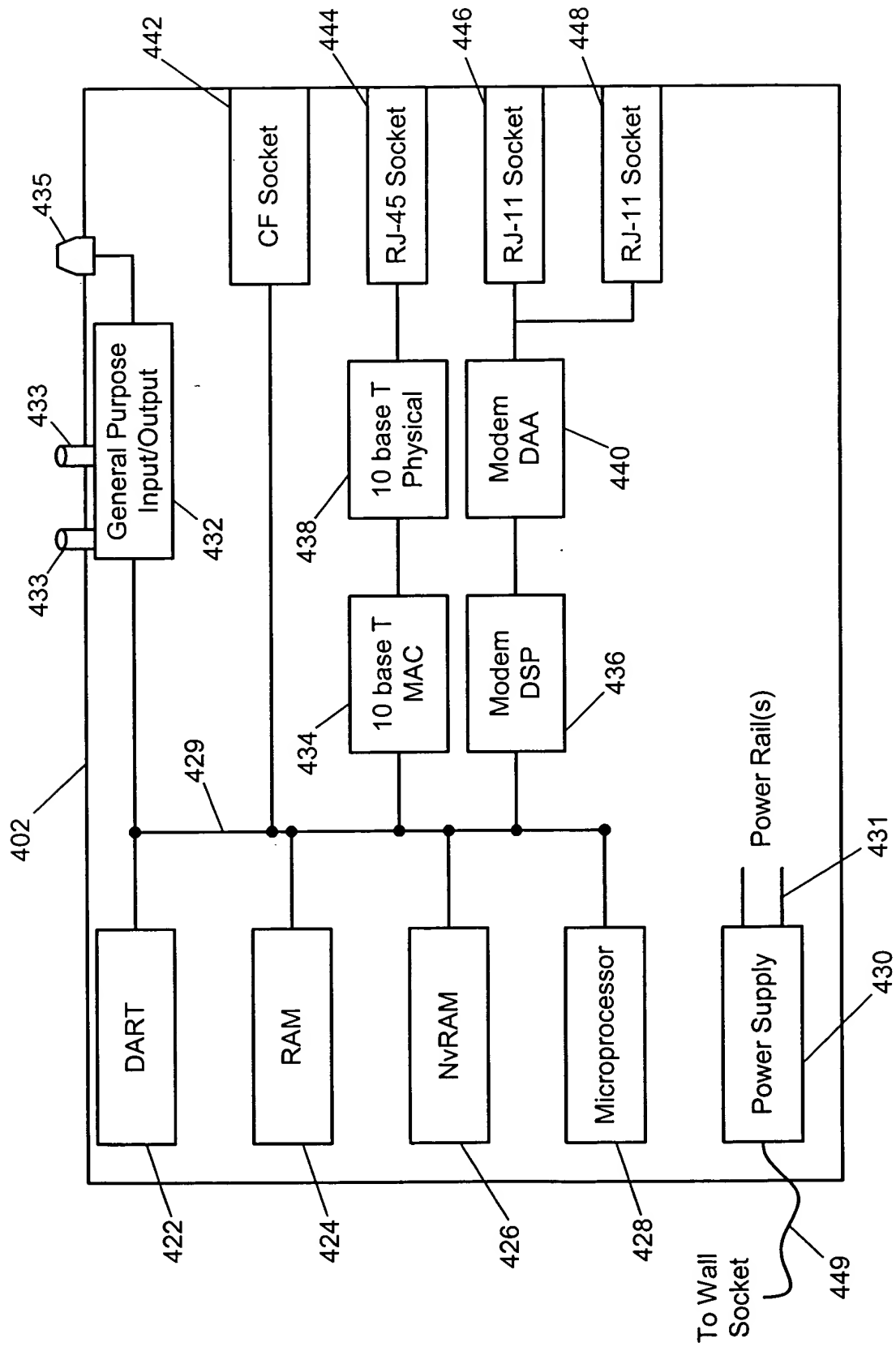


Figure 3

FIG. 4 is a block diagram of a system 400 in accordance with the present invention.



400

Figure 4

FIG. 5 is a block diagram of a computer system 500.

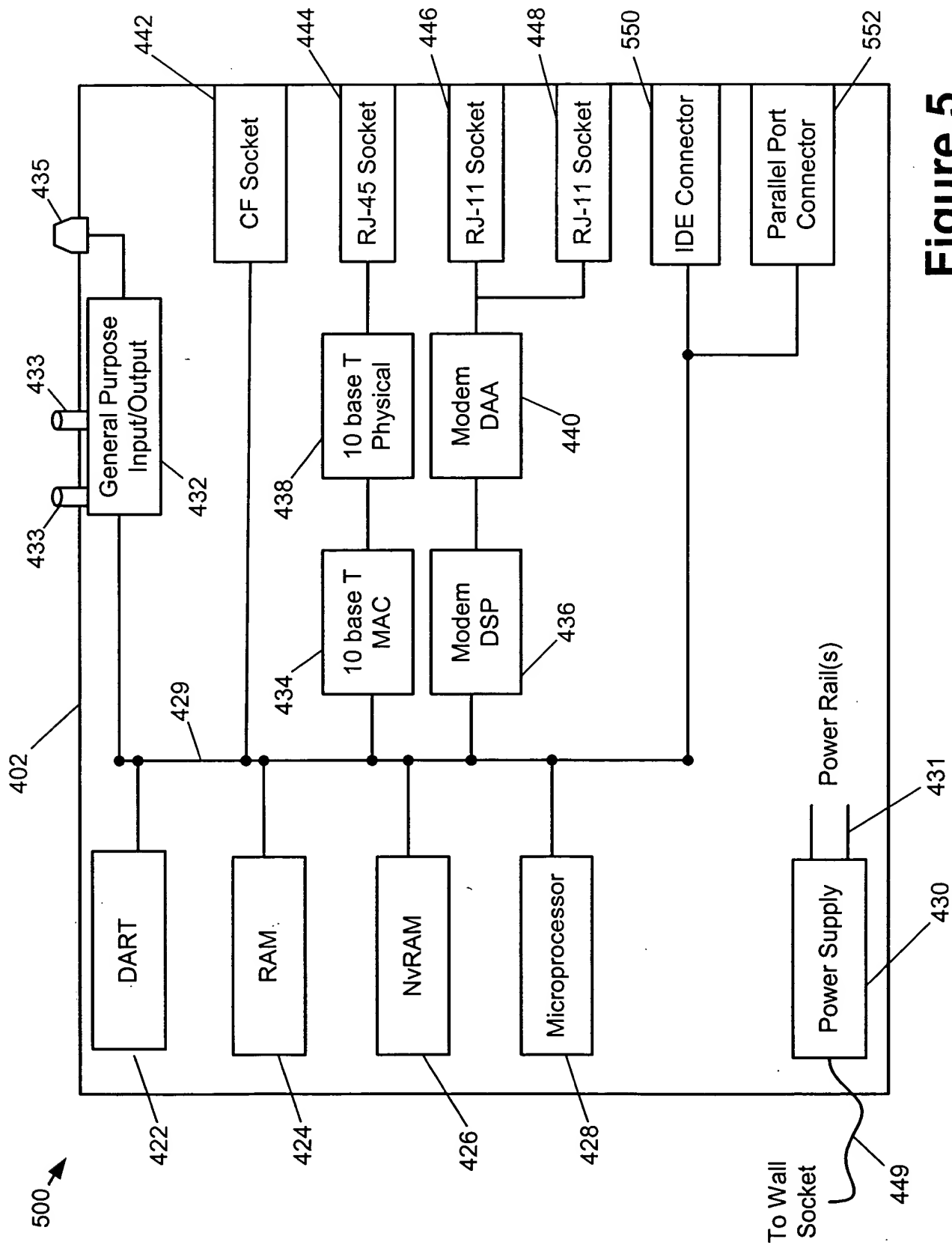


Figure 5

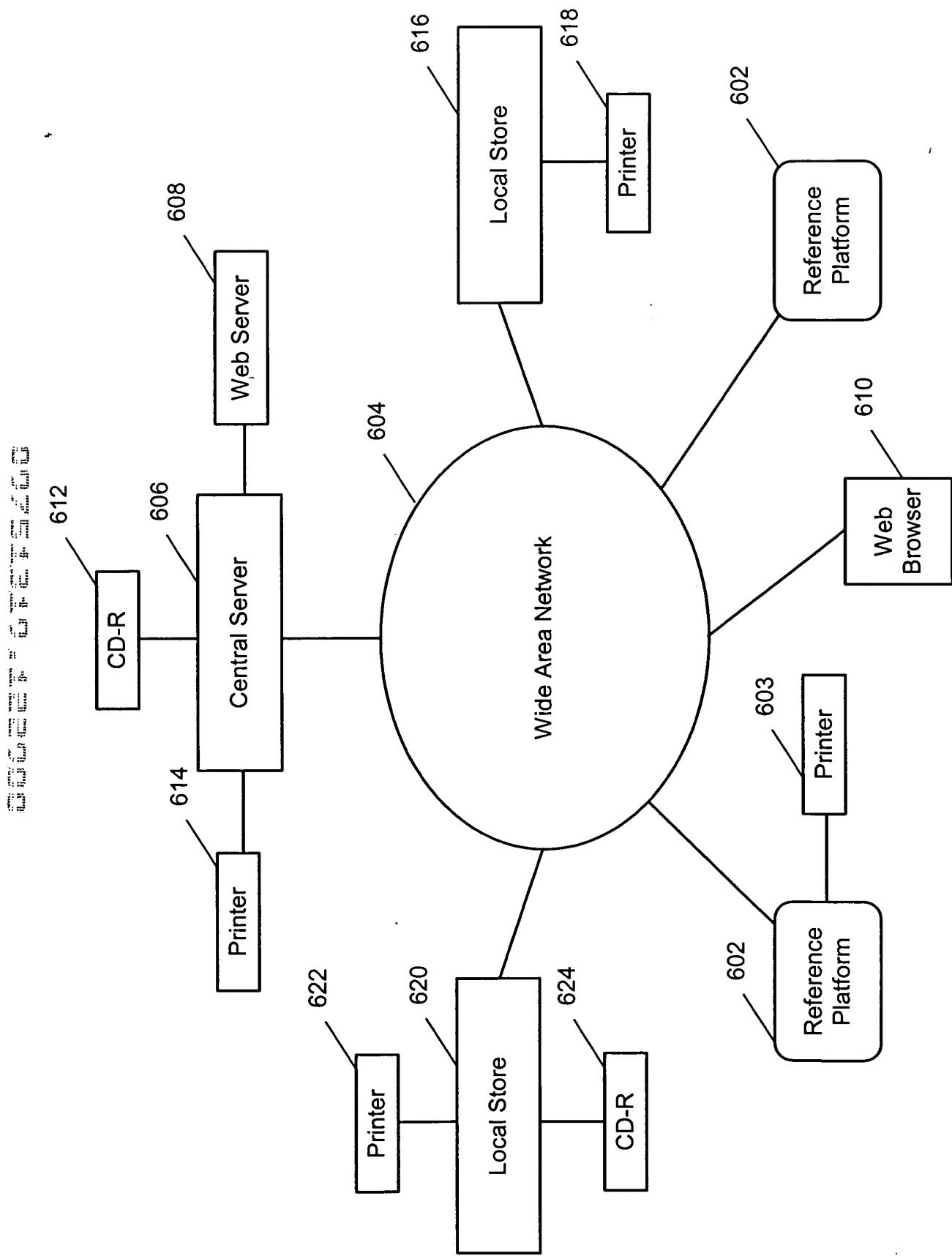


Figure 6

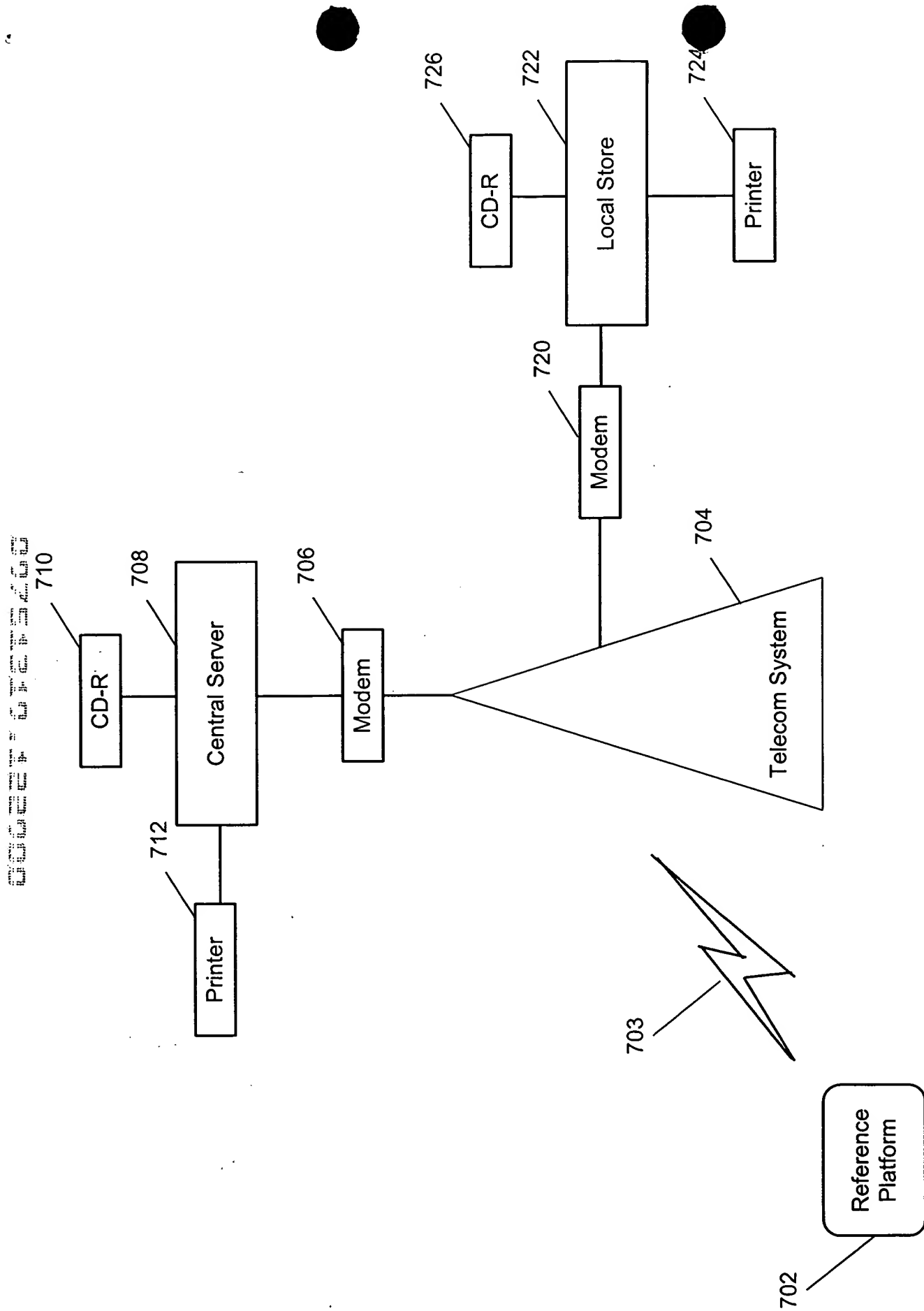


Figure 7

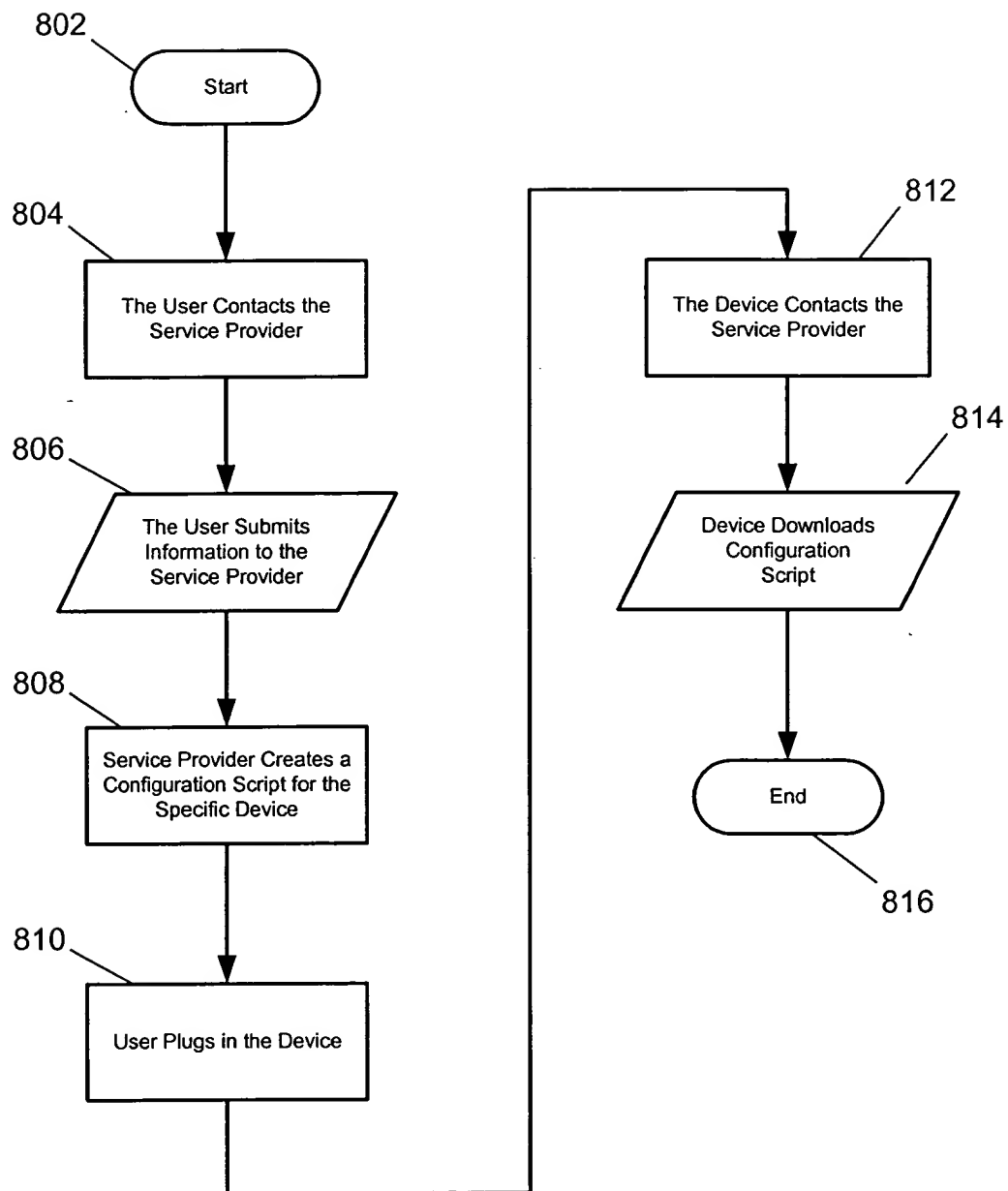


Figure 8

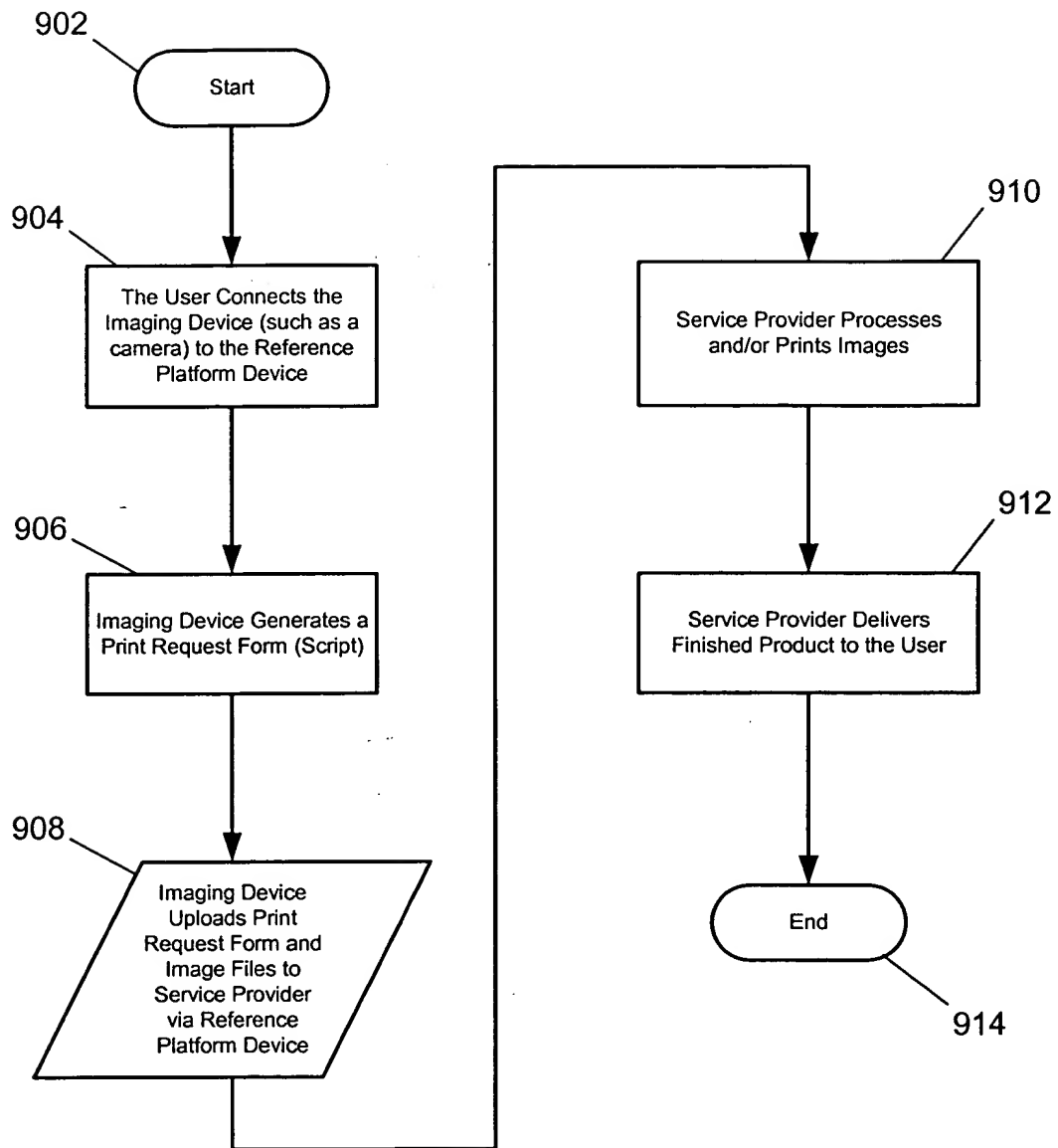


Figure 9